

Ubuntu for Renesas RZ/G3S, G3E - Single image supports various boards

Release Note (x03)

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Purpose

The Ubuntu Core 26 images are the Early Access image for the RZ/G3S, G3E platform.

Certification for the RZ/G3S, G3E platforms are **in progress** and will be provided in a future update. Until certification is completed, the image may be used on RZ/G3S, G3E for **development and evaluation purposes**.

Please note that, for Ubuntu Desktop 26.04, minimum 4GBs of RAM and 16GB storage are required.

The kernel included in this release is intended only for testing purposes.

Production deployments on RZ/G3S, G3E, V2N and V2H are recommended once Ubuntu Certification becomes available.

Production deployments and distribution of Ubuntu images requires purchasing [Ubuntu Pro](#). [Contact Canonical](#) for further information

Image download and Installation instructions

Refer to <https://ubuntu.com/download/renesas-iot> and download the latest image installation instructions for the target platform.

Tested Hardware Platforms

- Renesas RZ/G3S
- Renesas RZ/G3E

List of Features enabled in this release

Devices/ Features	Features Description	Feature Implementation status	
		RZ/G3E	RZ/G3S
Bootloader	Provided by Renesas	Supported	Supported
GPU	Supported with mesa and Panfrost	Supported	Not available
microSD card (first sd slot)	microSD card reader on the carrier board	Supported	Supported
Display	microHDMI	Supported	Not available
Ethernet		Supported	Supported
USB-A		Supported	Supported
Camera	MIPI Camera	Not supported	Not available
DRP-AI3		Not available	Not available
• Not available: no hardware device presented on the board • Not supported: Not working in current release and to be supported in future release			

Known Issues

- IP not assigned on second reboot due to changing MAC
 - This affected EVK that didn't have a permanent MAC configured.
 - Workaround: Refer to Section 3. Tips in [image installation instruction](#)
- [Ubuntu Desktop] Audio is not working on Desktop environment
- Secure Boot and FDE require the secure component in the SoC and explicit setup. Please contact Canonical for more information.